

INTERFERENCE SUPPRESSOR FOR SUPPRESSING HIGH-FREQUENCY
INTERFERENCE EMISSIONS FROM A DIRECT CURRENT MOTOR THAT IS
DRIVABLE IN A PLURALITY OF STAGES AND/OR DIRECTIONS

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Abstract

An interference suppressor (10) for suppressing high-frequency interference emissions of a direct current motor (26) that is drivable in a plurality of stages 10 and/or directions is proposed, having a plurality of capacitors (16) located on a first side (12) of a printed circuit board (14) and having a plurality of first conductor tracks (18), located on the first side (12) of the printed circuit board (14), for putting the various capacitors (16) into contact with a ground terminal (20), and having a first terminal (22) and at least one further terminal (24) for the individual 15 stages of the direct current motor (26), the first terminal (22) and the at least one further terminal (24) being put into contact with a first connection line (48) for the first stage and at least one further connection line (50) for the at least one further stage of the direct current motor (26). The interference suppressor (10) is characterized in that a ground face (34) is located on a further side (32), 20 diametrically opposite the first side (12), of the printed circuit board (14), and the first connection line (48) and the at least one further connection line (50) are fed through in insulated fashion relative to the ground face (34).